

FIG. 1

10

ROW ADDRESSES

110

100

ADDRESS DECODER 120

130

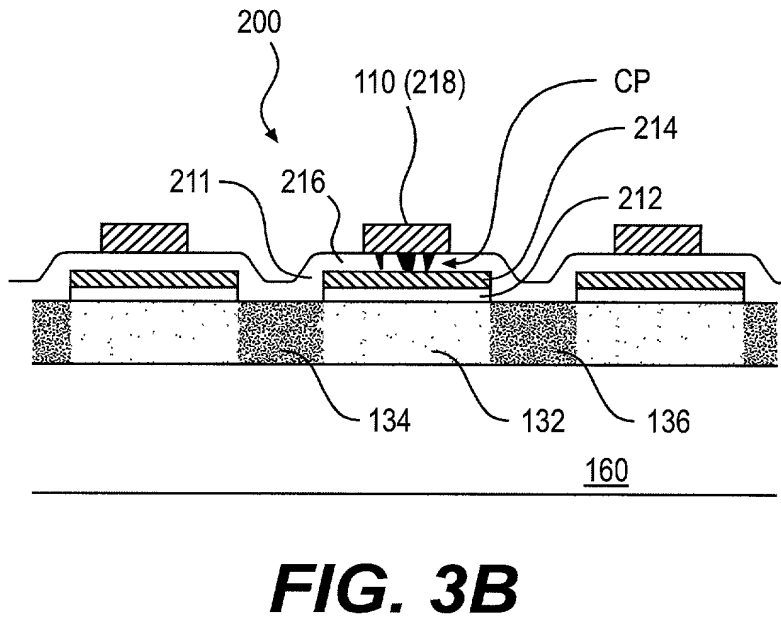
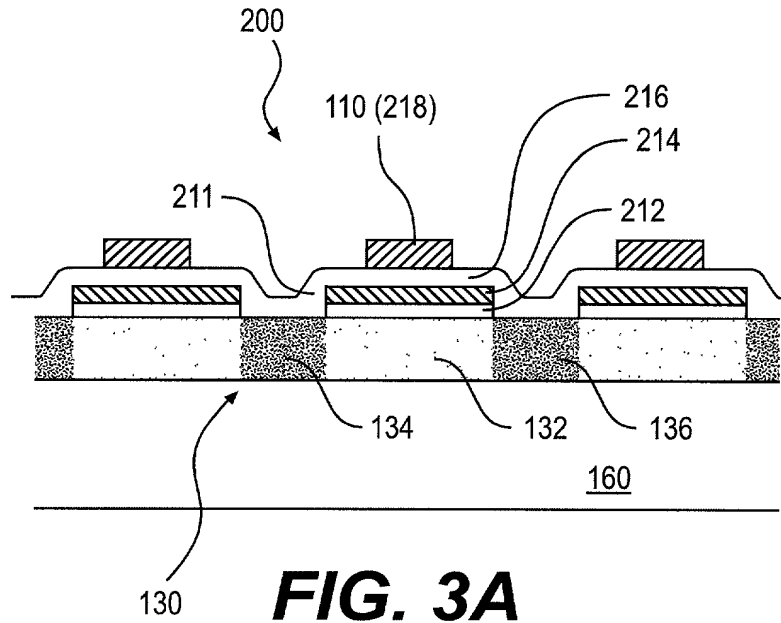
I/O

MULTIPLEXER 140

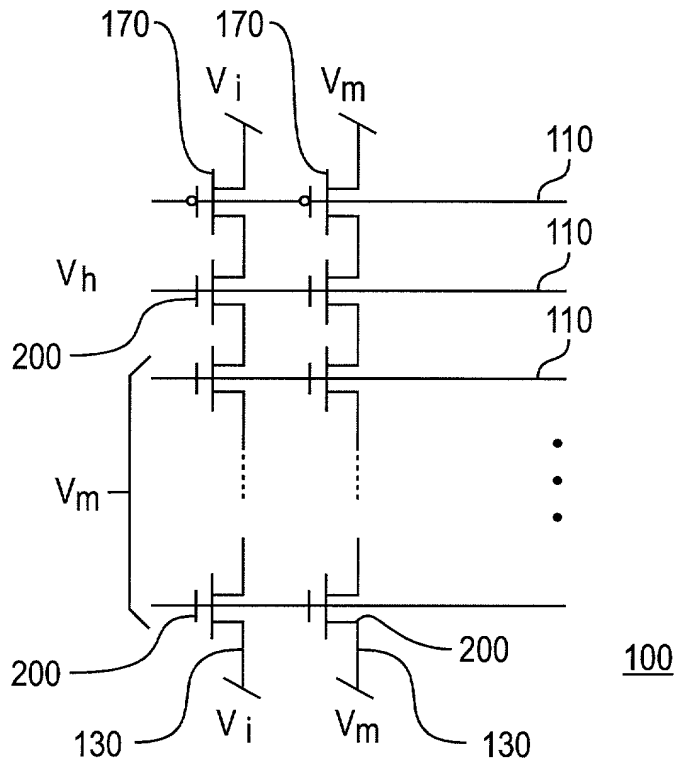
DATA ADDRESSES

150

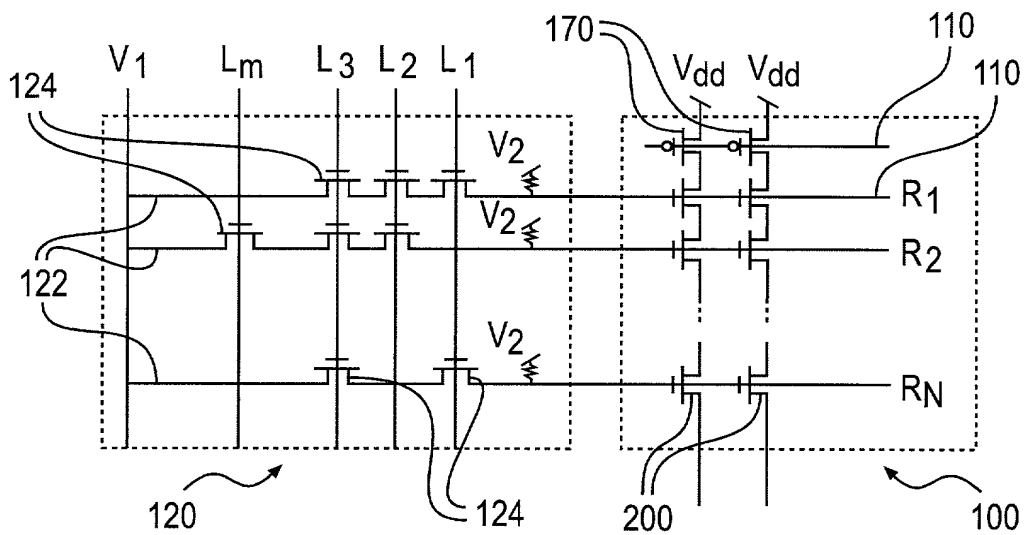




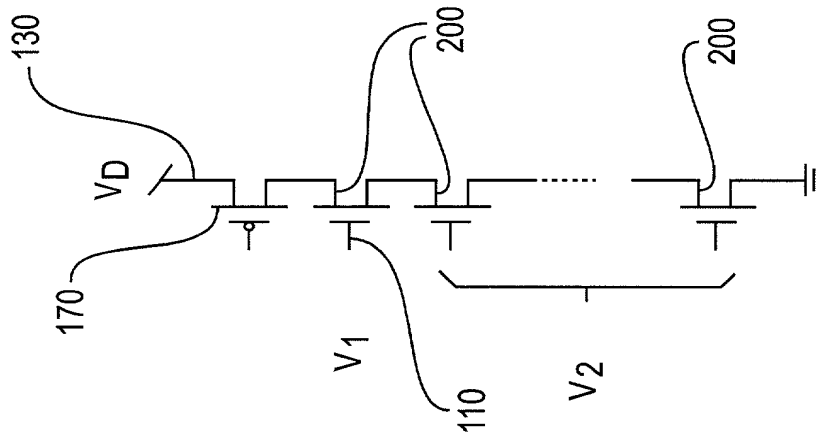




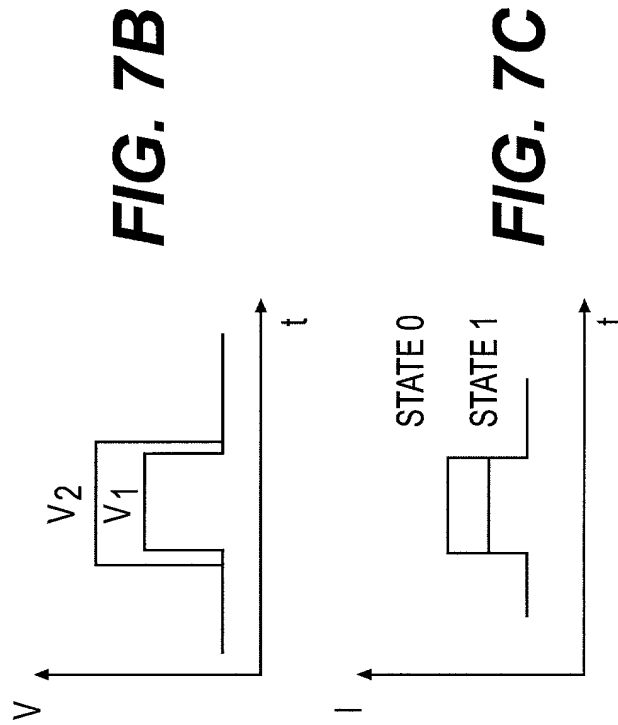
**FIG. 6**

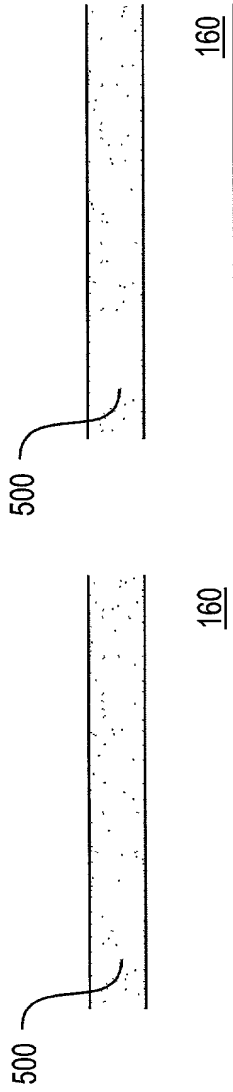


**FIG. 8**



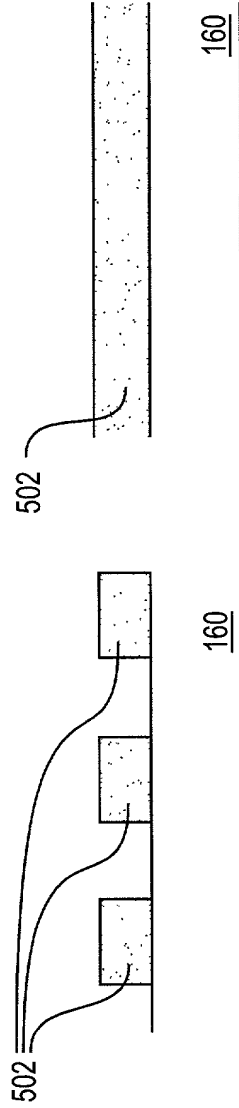
**FIG. 7A**





**FIG. 9A**

**FIG. 9B**



**FIG. 10A**

**FIG. 10B**

160

**FIG. 11A**

160

**FIG. 11B**

160

**FIG. 12A**

160

**FIG. 12B**

FIG. 2B0" 2454E560

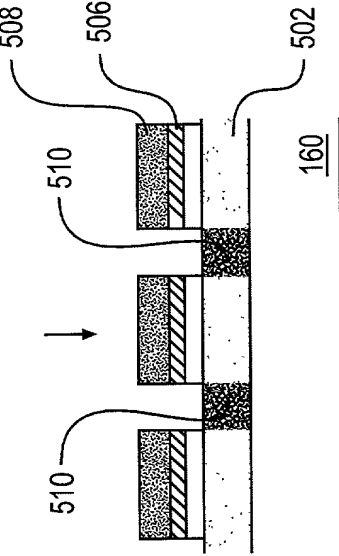


FIG. 13B

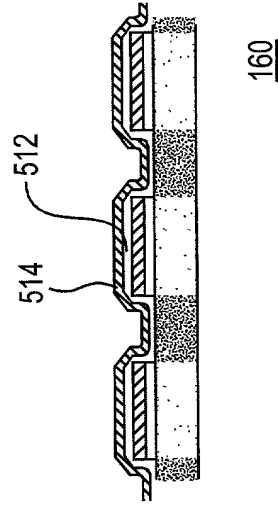


FIG. 14B

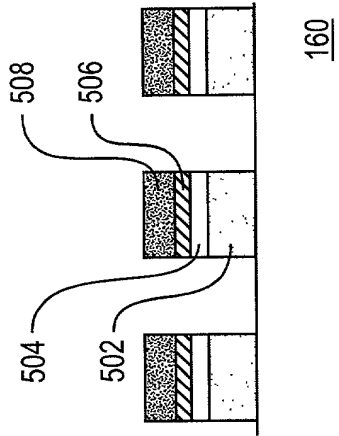


FIG. 14A

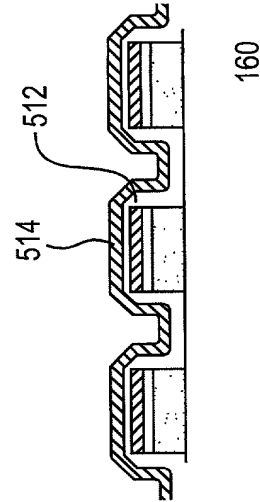


FIG. 14A



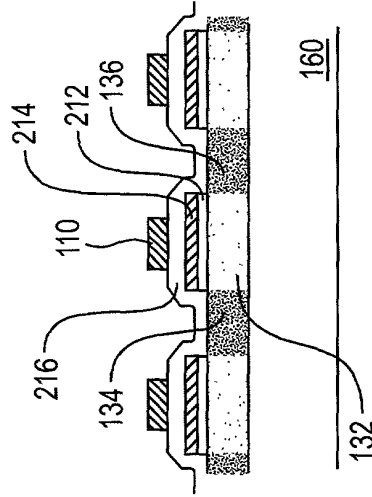


FIG. 15B

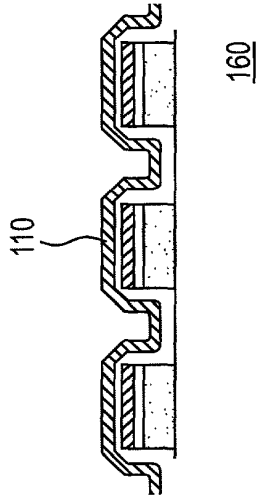


FIG. 15A